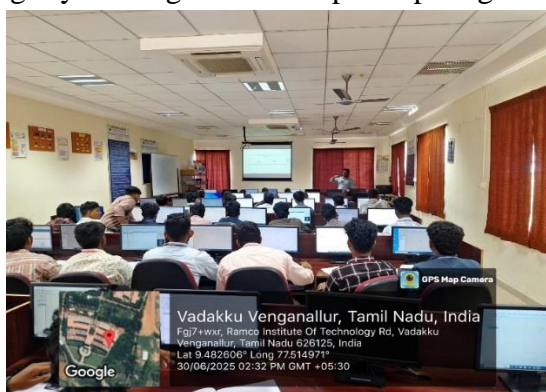


RAMCO INSTITUTE OF TECHNOLOGY
Department of Electrical and Electronics Engineering
Academic Year 2025– 2026
IE(I) EE Student chapter - Details of Events Conducted

Value Added Course on “Electronic Gadgets Design and Development Cycle”

On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter conducted the Five-days **Value Added Course on “Electronic Gadgets Design and Development Cycle”** from 30.06.2025 to 04.07.2025. The program was conducted by **Mr. Raghunagam, Shree Vision Enterprises, Madurai**. The 43 students of I year EEE, actively participated and benefited. This value-added course is organized to provide knowledge about the basics of electronic components and simulation software. The overall feedback of the program was good and participants told me that they enjoyed the course thoroughly and were eagerly looking forward to participating in similar programs in the future.



Resource person delivering the session

Technical Event – Group Discussion

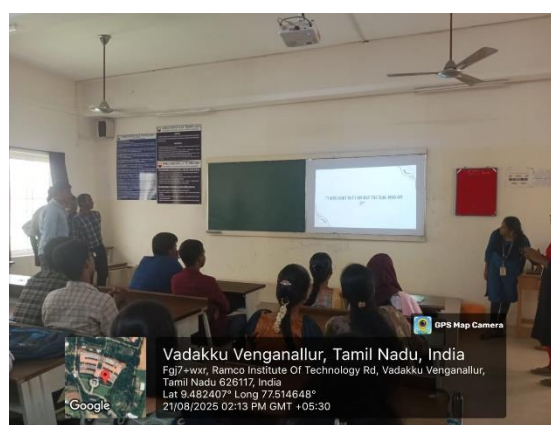
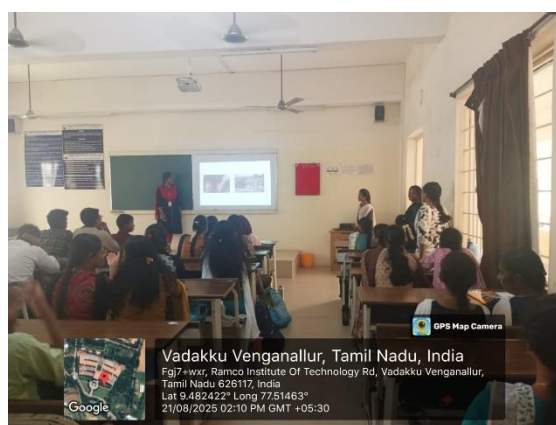
On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter was conducted the event **Group Discussion** on August 6, 2025. The event was conducted by **Dr. S. Jeyanthi, AP-III/EEE, Ramco Institute of Technology, Rajapalayam**. The student coordinators are **Naveen S and Swetha V, III year EEE**. The group discussion topics are **Impact of ChatGPT on Modern E-Learning Platforms, Is Electric Vehicle Adoption the Right Step Toward a Greener Future?, The Impact of Social Media on Youth, Pros and Cons of Online Learning, The impact of global pandemics on society, The impact of climate change and what we can do to combat it, Is technology making us more isolated?.** Totally 60 students from III year EEE actively participated in this event.



Screenshot of event conducted

Technical Event– ElectroClue

The Department of Electrical and Electronics Engineering organized a technical event titled “**ElectroClue**” on 21.08.2025 from 01:55 PM to 02:40 PM for I Semester B.E. EEE students to enhance their technical knowledge, problem-solving skills, analytical thinking, and teamwork. The event consisted of four interesting rounds: Round 1 – Unscramble the Technical Word, where teams solved jumbled letters to form valid EEE-related terms; Round 2 – Guess the Scientist, where participants identified scientists or engineers from clues; Round 3 – Technical Riddles, which involved solving riddles based on devices, components, or laws; and Round 4 – Connections (Picture Connect), where four images were used to find a technical word or theme. The event was coordinated by Dr. S. Jeyanthi, AP-III / EEE, with student coordinators Ms. Hindhu Priya G, Ms. Kaviya Sree Sahana T, and Ms. Muthulingeswari M from IV year EEE of Ramco Institute of Technology, Rajapalayam.



Screenshot of event conducted

Guest Lecture on “Modeling of Thermal Energy Storage Systems”

On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter conducted **Guest Lecture on “Modeling of Thermal Energy Storage Systems”** on 01.09.2025 from 10.00 AM to 12.00 Noon at the EEE Seminar Hall. The resource person of the program was **Dr.Thangaraja Jeyaseelan, Professor, Automotive Research Centre, School of Mechanical Engineering, VIT, Vellore, India**. The Resource person explained the importance of energy storage in the context of sustainable energy systems. He provided detailed insights into the types of thermal energy storage, including sensible heat storage, latent heat storage, and thermochemical storage. A total of 63 students from III Year EEE participated in the program and greatly benefitted from the session.



Resource person delivering the session

Poster Competition on “Application of IoT in Electrical Engineering”

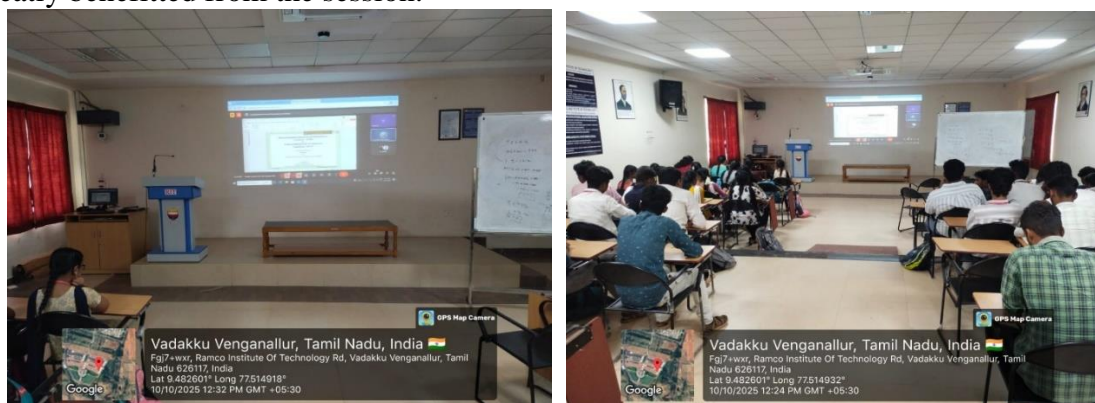
On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter was conducted the **Poster Competition on “Application of IoT in Electrical Engineering”** on 03rd September 2024. The interested students of EEE were divided into 20 groups comprises of 1 or 2 students, so totally 36 students actively participated in this event. Dr. K. Karthikeyan, Professor/EEE & Dr. S. Vairaprakash, Associate Professor/ECE, Ramco Institute of Technology, Rajapalayam evaluated the poster competition and announced the prize winners of this contest. The event was conducted by Dr. S. Jeyanthi, AP-III, Department of EEE, Ramco Institute of Technology, Rajapalayam. The student coordinators were Nagussan N, IV year EEE and Krishna B, III year EEE.



Screenshot of event conducted

Guest Lecture on “Integration of Electric Vehicles into Smart Grids: Opportunities and Challenges”

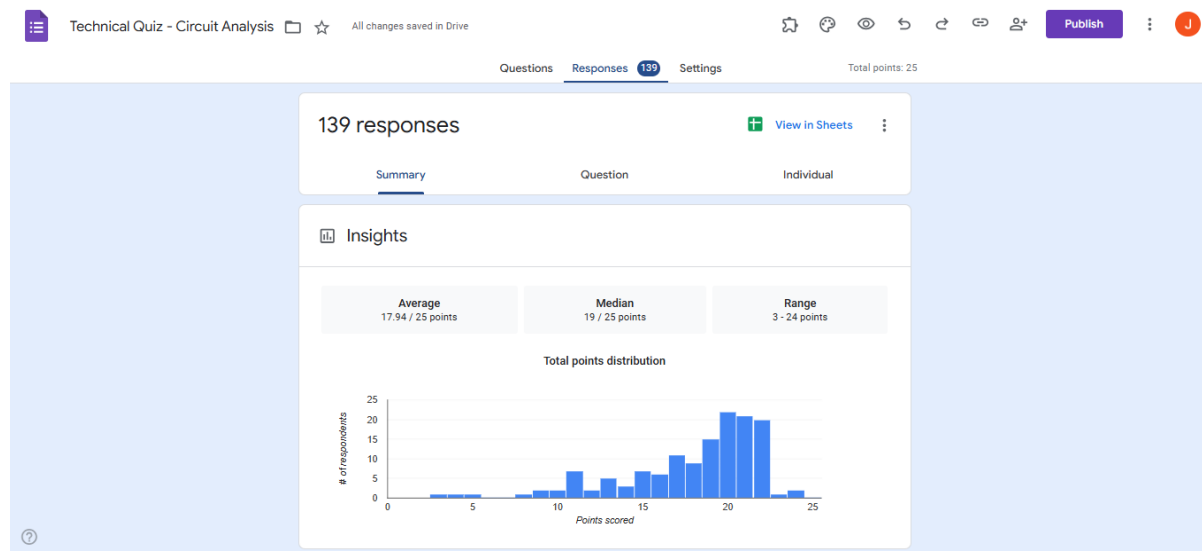
On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter conducted Online Guest Lecture on “**Integration of Electric Vehicles into Smart Grids: Opportunities and Challenges**” on 10.10.2025 from 12.20 PM to 01.20 PM at the EEE Seminar Hall. The resource person of the program was **Dr. C. Anandhakumar, Assistant Professor- Selection Grade, Department of EEE, Sri Ramakrishna Institute of Technology, Coimbatore**. The Resource person explained the evolution of smart grids and the growing role of electric vehicles in modern power systems. He discussed various aspects such as EV charging infrastructure, grid integration challenges, bidirectional power flow, vehicle-to-grid (V2G) technology, and the importance of load management and communication systems in smart grids. A total of 55 students from IV Year EEE participated in the program and greatly benefited from the session.



Resource person delivering the session

Technical quiz on “Circuit Analysis”

On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter was conducted the event **Technical quiz on “Circuit Analysis”** on 15th October 2025. The event was conducted by Dr. S. Jeyanthi, AP-III/EEE, Ramco Institute of Technology, Rajapalayam. A total of 139 students from the III and IV years of the EEE and ECE departments actively participated in this event.



Technical quiz on “Digital Electronics”

Aptitude test on “Quantitative Aptitude”

On behalf of Ramco Institute of Technology (RIT), the Department of Electrical and Electronics Engineering, in association with the IE(I) EE Student Chapter, conducted an aptitude test on “Quantitative Aptitude” on February 26, 2026. The event was organized and conducted by Dr. S. Jeyanthi, AP-III/EEE, Ramco Institute of Technology, Rajapalayam.

A total of 438 students from Ramco Institute of Technology actively participated in the event. Based on their performance in Round 1, 26 students were shortlisted for Round 2. The second round was conducted on March 13, 2026. Based on the final evaluation, Ms. Prathisha S, I Year AI & DS, secured the First Prize. Ms. M. Pooja, II Year AI & DS, secured the Second Prize. Ms. Sharu Mahima S, I Year AI & DS, secured the Third Prize.



Screenshot of event conducted

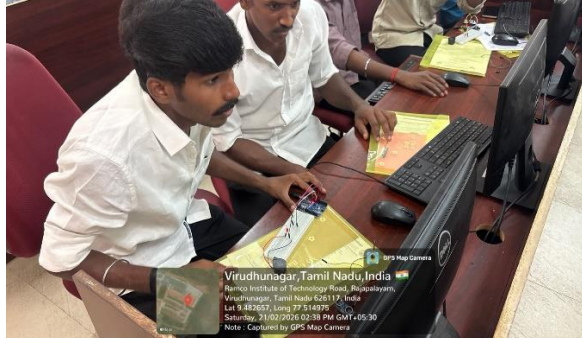
NATIONAL LEVEL EVENTS

National Level Workshop on Internet of Things (IoT)

The Workshop on Internet of Things (IoT), conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium at Ramco Institute of Technology. The session was delivered by **Dr. N. Pothirasan, Adjunct Professor, EEE, Ramco Institute of Technology**, and witnessed active participation from 41 students representing 13 institutions. The workshop aimed to bridge the gap between theoretical knowledge and real-world industrial applications of IoT. The hands-on sessions enabled participants to build real-time prototypes, enhancing their understanding of automation systems. The resource person's clear explanations and interactive teaching approach were widely appreciated.



Workshop – Session in Progress



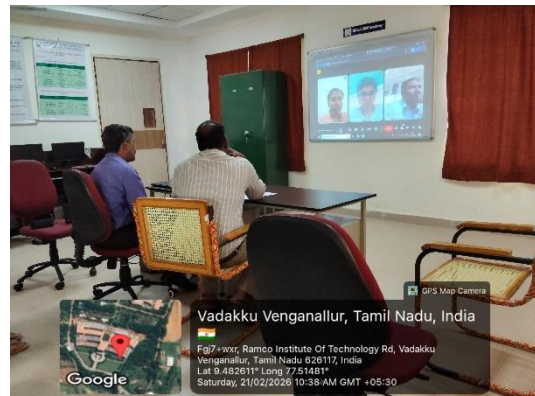
Hands-on Hardware Interfacing

National Level Paper Presentation on “Application of AI in Electrical Engineering”

The Paper Presentation event, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was held in hybrid mode at the EEE Seminar Hall and NI LabVIEW Laboratory. A total of 45 participants from 13 institutions presented their research work. The event was evaluated by a distinguished jury panel comprising **Dr. G. Pandiaraj (AP-SL Grade/EEE, Mepco Schlenk Engineering College)**, **Mr. B. Kannan (AP-SG/ECE)**, **Dr. K. Kathikeyan (Professor/EEE)**, and **Dr. P. Prem (ASP-III/EEE, Ramco Institute of Technology)**. The jury evaluated the presentations based on originality, technical depth, clarity, and relevance. The event encouraged research-oriented thinking, improved presentation skills, and promoted interdisciplinary knowledge. Overall, it served as an excellent platform for academic exchange and innovation.



Paper Presentation – EEE Seminar Hall



Paper Presentation – NI LabVIEW Laboratory

National Level Project Contest

The Project Contest, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was held at the Control and Instrumentation Laboratory with participation from 27 students representing 6 institutions. The event was judged by **Mr. A. Guna, Lecturer/EEE, Sankar Polytechnic College, Tirunelveli, and Dr. R. Ramalakshmi, AP-III/ECE, Ramco Institute of Technology.**

Participants presented innovative projects addressing real-world challenges through working prototypes and structured demonstrations. The evaluation criteria included creativity, technical knowledge, implementation quality, scalability, and clarity of presentation. The jury members actively interacted with participants to assess their depth of understanding and practical skills. The event provided a valuable platform for students to showcase their engineering capabilities and encouraged them to apply theoretical knowledge to real-life applications. It successfully promoted innovation, teamwork, and problem-solving skills among participants.

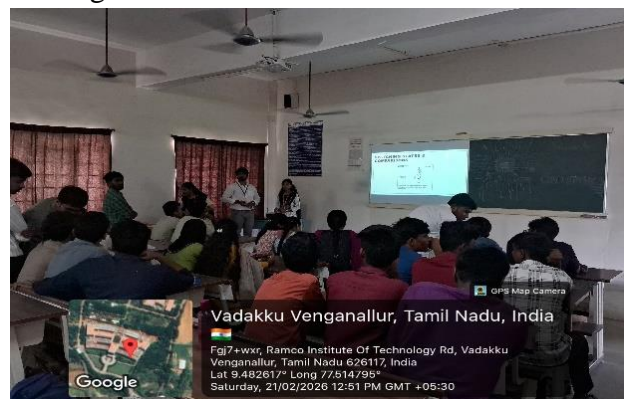


Screenshot of event conducted

National Level Event on Circuitronics

Circuitronics, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was a technical competition designed to test participants' analytical and debugging skills in electronics. The event witnessed participation from 32 students representing 7 institutions and was evaluated by **Dr. A. Arun Kumar, ASP-I/EEE, Ramco Institute of Technology.**

The competition comprised three rounds: "Bingo Cards," testing fundamental electronics knowledge; "Circuit Sense & Logic Link," focusing on circuit analysis and application; and "Fix it Fast," which required participants to identify and rectify faults in circuits under time constraints. The event effectively simulated real-world engineering challenges and enhanced participants' problem-solving abilities. It provided a competitive platform for strengthening core concepts in circuit analysis and troubleshooting.



Screenshot of event conducted

National Level Event on Tech No Tech

Tech No Tech, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was an innovative hybrid event combining technical and non-technical elements. The event included 28 participants from 7 institutions and was judged by **Mrs. G. Sivapriya, AP-II/EEE, and Dr. S. Narthana, AP-I/EEE, Ramco Institute of Technology.**

The event comprised three rounds: “Film Fusion,” which tested observation skills; “Brain Spark,” which assessed technical knowledge of electrical concepts; and “Electro Hunt,” a hands-on round requiring circuit assembly using electronic components. This unique format encouraged creativity, teamwork, and technical understanding. The event highlighted the importance of holistic thinking in engineering and was widely appreciated for its engaging structure.



Screenshot of event conducted

National Level Event on Master In You

Master In You, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was an interactive cognitive challenge with participation from 14 students representing 5 institutions. The event was judged by **Mr. S. Meenakshi Sundaravel, AP (SG)/EEE, Ramco Institute of Technology.**

The event included three rounds: “Quick Fix,” where participants identified movie songs using visual clues; “Team Up,” which tested analytical thinking; and a multitasking round that evaluated participants’ ability to perform multiple tasks simultaneously. The event provided an opportunity for participants to demonstrate their cognitive and creative abilities beyond technical knowledge. It encouraged quick thinking, concentration, and teamwork, making it a highly engaging and intellectually stimulating session.



Screenshot of event conducted

National Level Event on Fun On

Fun On, conducted on February 21, 2026, as part of VIDYANTRA '26 National Level Technical Symposium, was a non-technical recreational event with participation from 40 students representing 9 institutions. The event was evaluated by **Dr. A. S. Vigneshwar, AP-III/EEE, Ramco Institute of Technology**.

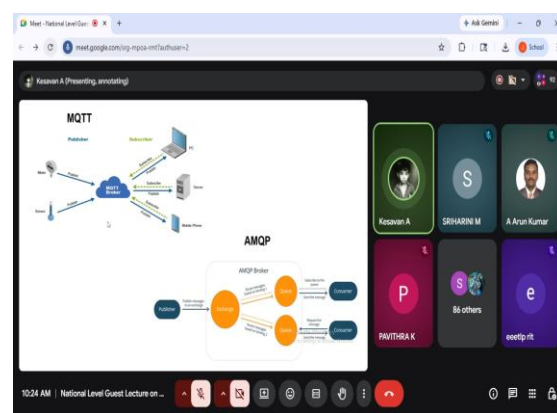
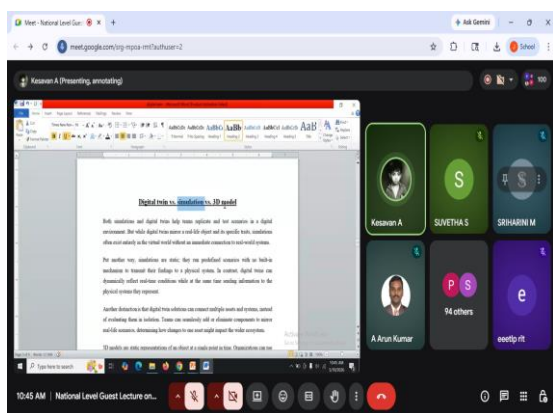
The event consisted of three rounds: identifying humorous situations, performing opposite actions to given instructions, and connecting images to determine a common theme. These activities tested participants' creativity, observation skills, and presence of mind. The event created a lively and interactive atmosphere, encouraging participation and collaboration. It provided a refreshing break from technical sessions while maintaining active engagement and enthusiasm among participants.



Screenshot of event conducted

National Level Online Guest Lecture on the topic “Digital Twins in Electrical and Electronics Engineering”

On behalf of RIT, Department of Electrical and Electronics Engineering, IE(I) EE student chapter conducted National Level Online Guest Lecture on the topic “Digital Twins in Electrical and Electronics Engineering” on May 10, 2026 from 10:00 AM to 11:00 AM through Google meet. The resource person of the program was **Mr. Kesavan, CEO, Quantanics TechServ Pvt. Ltd., Madurai**. The event was coordinated by Dr. A. Arun Kumar, ASP-I/EEE. The session was highly interactive and informative. A total of 101 internal and external students participated in the program and greatly benefited from the session. The lecture provided valuable industrial exposure and enhanced the technical knowledge of the participants regarding modern digital technologies in engineering applications.



Screenshot of event conducted